

# Dr. Manjanna B

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## Educational Qualification

PhD in Computer Science,  
Indian Institute of Technology Guwahati

MTech in Computer Science and Engineering,  
National Institute of Technology Karnataka, Surathkal

BE in Information Science and Engineering,  
University Visvesvaraya College of Engineering, Bangalore

## Research Interests

Algorithms, Computational Geometry

## Publications (Journals)

Manjanna Basappa and Gautam K. Das, “Discrete Unit Square Cover Problem”, Discrete Mathematics, Algorithms and Applications (World Scientific), 2018. (accepted)

Manjanna Basappa, Rashmisnata Acharyya and Gautam K. Das, “Unit Disk Cover Problem in 2D”, Journal of Discrete Algorithms (Elsevier), Vol. 33, pp. 193-201, 2015.

## Publications (Conference Proceedings)

Manjanna Basappa, “Line Segment Disk Cover”, Proceedings of the 4th International Conference on Algorithms and Discrete Applied Mathematics (CALDAM 2018), Guwahati, India, February 12-17, 2018, LNCS 10743 (Springer-Verlag), pp. 81-92, 2018.

Manjanna Basappa, Ramesh K. Jallu, Gautam K. Das and Subhas C. Nandy, “The Euclidean  $k$ -Supplier Problem in  $R^2$ ”, Proceedings of the 12th International Symposium on Algorithms and Experiments for Wireless Sensor Networks (ALGOSENSORS 2016), Aarhus, Denmark, August 25-26, 2016, LNCS-10050 (Springer-Verlag), pp. 129-140, Revised Selected Papers 2017.

Manjanna Basappa, Ramesh K. Jallu and Gautam K. Das, “Constrained  $k$ -Center Problem on a Convex Polygon”, Proceedings of the 15th International Conference on Computational Science and its Applications (ICCSA 2015), Banff, Alberta, Canada, June 22-25, 2015, LNCS-9156 (Springer-Verlag), pp. 209-222, 2015.

Rashmisnata Acharyya, Manjanna Basappa and Gautam K. Das, “Unit Disk Cover Problem in 2D”, Proceedings of the 13th International Conference on Computational Science and its Applications (ICCSA 2013), Ho Chi Minh City, Vietnam, June 24-27, 2013, LNCS-7972 (Springer-Verlag), pp. 73-85, 2013.

## **Awards & Achievements**

Qualified GATE 2009 in Computer Science & Engineering with Percentile 99.23 and All India Rank 320.

Qualified JEST 2011 in Computer Science with Rank 83.

Awarded GATE Scholarship from MHRD, Government of India, for pursuing MTech at NITK Surathkal, India.

Awarded Research Fellowship from MHRD, Government of India, for pursuing PhD at IIT Guwahati, India.

## **Work Experience**

### **Asst. Professor: (July 2018 – )**

Department of CSE, Indian Institute of Information Technology Dharwad, Karnataka.

### **Staff Engineer: (May 2018 – July 2018)**

RLE India ME Pvt Ltd, ITPL, Bangalore, Karnataka.

### **Asst. Professor: (January 2017 – April 2018)**

School of CSE, Vellore Institute of Technology (VIT University), Vellore, Tamilnadu.

### **Asst. Professor: (June 2016 – January 2017)**

Department of CSE, M S Ramaiah University of Applied Sciences, Bangalore, Karnataka.

### **Research Fellow: (December 2011 – May 2016)**

Department of Mathematics, Indian Institute of Technology Guwahati, Assam.